<u>IN THE UNITED STATES PATENT AND TRADEMARK OFFICE</u>

Applicant: Michael Pearson

Title: A ROW DECODING SCHEME FOR DOUBLE SAMPLING IN 3T PIXEL ARRAYS

Docket No.:   33981US1

<u>PRELIMINARY AMENDMENT</u>

Commissioner for Patents
Washington, DC 20231

Sir:

Please enter this amendment prior to the examination of the above-referenced patent application. Please amend the application as follows.

<u>IN THE CLAIMS:</u>

Please amend claims 10 and 11 to read as follows.

1   10. (Amended) Apparatus as claimed in claim 8 wherein the light sensitive elements are

2   CMOS Pixels.

1   11. (Amended) Apparatus as claimed in claim 8 wherein the light sensitive elements are

2   three transistor CMOS Pixels.

Please add new claims 18 and 19 to read as follows.

1   18. (New) Apparatus as claimed in claim 9 wherein the light sensitive elements are CMOS

2   Pixels.

1   19. (New) Apparatus as claimed in claim 9 wherein the light sensitive elements are three

2   transistor CMOS Pixels.

REMARKS

Claims 10 and 11 have been amended and new claims 18 and 19 have been added to eliminate multiple dependency.

If any fees are required by this communication, please charge such fees to our Deposit Account No. 160820 Order No. 33981US1.

Respectfully submitted,

PEARNE & GORDON LLP

By _____
     John P. Murtaugh, Reg. No. 34226

526 Superior Avenue East
Suite 1200
Cleveland, Ohio 44114-1484
(216) 579-1700

Date: _10 - 4-01_____

1   10. (Amended) Apparatus as claimed in [claims 8 or 9] <u>claim 8</u> wherein the light sensitive

2   elements are CMOS Pixels.

1   11. (Amended) Apparatus as claimed in [claims 8 or 9] <u>claim 8</u> wherein the light sensitive

2   elements are three transistor CMOS Pixels.